**CSCI 360-1 Quiz 2 Name\_\_\_\_KEY\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_**

**Spring 2019 10 points 8 March 2019**

**Given the following ABEND dump, answer the questions below.**

PSW AT ABEND FFC50006 80000014 Reg 15 = 00000000

Reg 1 = 00000010, Reg 2 = 00000020, Reg 13 = 00000048, Reg 14 = FFFE7960

Addr Contents

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000000 5810F02C F820F030 1B21D500 F040F040 5010F036 D202F028 F04107FE FFFFFFFF

000020 FFFFFFFF FFFFFFFF D5D640F5 00000010 00000030 F5F5F5F5 F5F5F5F5 F5F5F5F5

000040 C1E8C5E2 F5F5F5F5 F5F5F5F5 00000000 F5F5F5F5 F5F5F5F5 F5F5F5F5 F5F5F5F5

1. What is the address of the instruction that WOULD HAVE BEEN EXECUTED NEXT had the program

not ABENDed on the current instruction? \_\_\_**000014**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

1. What is the length (in bytes) of the instruction that caused the ABEND? \_\_\_**4**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

1. What is the condition code? (Answer with a decimal number!) \_\_\_**0**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

1. What is the address of the instruction that caused the ABEND? \_\_\_\_**000010**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_
2. Write the instruction that caused the ABEND as the programmer would have written it in EXPLICIT

Assembler language using decimal values. \_\_\_\_\_**ST 1,54(,15)**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

(2 points)

1. What type of program interrupt (S0C?) occurred?

Number \_\_\_**6**\_\_\_\_\_\_\_\_\_\_\_ Name \_\_\_\_**Specification Exception**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_

1. Write that NEXT instruction that would have been executed as the programmer would have written it in

EXPLICIT Assembler language using decimal values. \_\_\_\_\_**MVC 40(3,15),65(15)**\_\_\_\_\_\_\_\_\_\_\_

(2 points)

1. What caused this ABEND?

\_\_\_**Store instruction references storage NOT on a FWB (fullword boundary).**\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_